

EAST - [Text_Search.wsp:1]

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DBs US-PGPUB; USPAT; EPO; JPO; DERWENT

Default operator: OR

☒ Plurals ☒ Highlight all hit terms initially

BRS:
 BRS:
 Pending
 Active
 L1: (1549) (OLED or (organic adj light adj emitting adj (diode or device))) and cathode and anode and (matrix or (column and row))
 L2: (144) 1 and ((scan\$4 and data) near3 driv\$3)
 L3: (5) 2 and (constant near2 current) and (high adj impedance) and ground\$3
 L4: (9) 1 and (constant near2 current) and (high adj impedance) and ground\$3
 L5: (7) 1 and ((horizontal and vertical) near2 synchroniz\$5)
 L7: (2) 6 and NOR and NAND
 L6: (64) 1 and register and logic
 L8: (33) 6 and (*315"/\$.ccls. or *345"/\$.ccls.)
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	E	Err
1	BRS	L1	1549	(OLED or (organic adj light adj emitting adj (diode or device))) and cathode and anode and (matrix or (column and row))	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 07:49			
2	BRS	L2	144	1 and ((scan\$4 and data) near3 driv\$3)	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 07:50			
3	BRS	L3	5	2 and (constant near2 current) and (high adj impedance) and ground\$3	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:06			
4	BRS	L4	9	1 and (constant near2 current) and (high adj impedance) and ground\$3	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:11			
5	BRS	L5	7	1 and ((horizontal and vertical) near2 synchroniz\$5)	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:15			
6	BRS	L7	2	6 and NOR and NAND	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:16			
7	BRS	L6	64	1 and register and logic	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:20			
8	BRS	L8	33	6 and (*315"/\$.ccls. or *345"/\$.ccls.)	US-PGPUB; USPAT, EPO, JPO, DERWENT	2004/11/09 08:21			